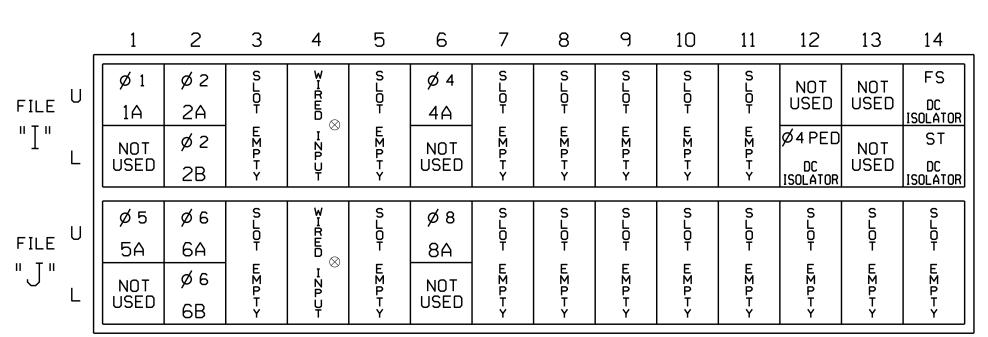


# INPUT FILE POSITION LAYOUT

(front view)



 $^{\otimes}$  Wired Input - Do not populate slot with detector card

EX.: 1A, 2A, ETC. = LOOP NO.'S

4. Connect serial cable from conflict monitor to comm. port 1 of 2070

controller. Ensure conflict monitor communicates with 2070.

FS = FLASH SENSE ST = STOP TIME

# LOAD RESISTOR INSTALLATION DETAIL

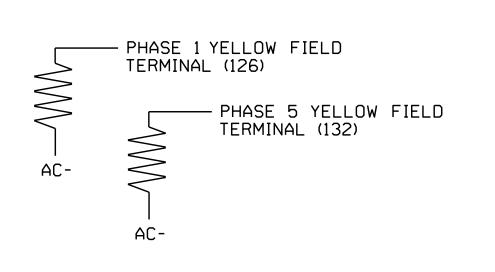
(install resistors as shown below)

ACCEPTABLE VALUES

VALUE (ohms) WATTAGE

1.5K - 1.9K 25W (min)

2.0K - 3.0K 10W (min)



#### NOTES

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- 2. Program phases 4 and 8 for Dual Entry.
- 3. Enable Simultaneous Gap-Out for all Phases.
- 4. Program phases 2 and 6 for Variable Initial and Gap Reduction.
- 5. Program phases 2 and 6 for Startup In Green.
- 6. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlap.
- 7. If this signal will be managed by an ATMS software, enable controller and detector logging for all detectors used at this location.
- 8. The cabinet and controller are part of the D01-07 Kitty Hawk System.

## EQUIPMENT INFORMATION

OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE

LOAD SWITCHES USED......S1,S2,S5,S6,S7,S8,S11,AUX S1,AUX S4
PHASES USED............1,2,4,4PED,5,6,8,8PED

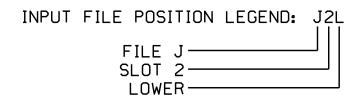
# INPUT FILE CONNECTION & PROGRAMMING CHART

L00P N0.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
	TB2-1,2	I1U	56	18	1	1	Υ	Υ			15
1A <sup>1</sup>	-	J4U	48	10 ★	26	6	Υ	Y	Y		3
	-	I1U	56	18 ★	51	1	Υ	Υ			3
2A	TB2-5,6	I2U	39	1	2	2	Υ	Υ			
2B	TB2-7,8	I2L	43	5	12	2	Υ	Υ			
4A	TB4-9,10	I6U	41	3	4	4	Υ	Υ			5
	TB3-1,2	J1U	55	17	5	5	Υ	Υ			15
5A <sup>2</sup>	-	I4U	47	9 ★	22	2	Υ	Υ	Υ		3
	-	J1U	55	17 ★	55	5	Υ	Υ			3
6A	TB3-5,6	J2U	40	2	6	6	Υ	Υ			
6B	TB3-7,8	J2L	44	6	16	6	Υ	Υ			
8A	TB5-9,10	J6U	42	4	8	8	Υ	Υ			
PED PUSH BUTTONS							NO	ΓE:			
P41,P42	TB8-5,6	I12L	69	31	PED 4	4/8 PED		INSTALL	DC I	SOLATOR	
	•	•							JT FIL	E SLOT	
								[12.			

Add jumper from I1-W to J4-W, on rear of input file.

<sup>2</sup>Add jumper from J1-W to I4-W, on rear of input file.

★ See Input Page Assignment programming details on sheets 3 and 4.



PROJECT REFERENCE NO. SHEET NO. Sig.1.1

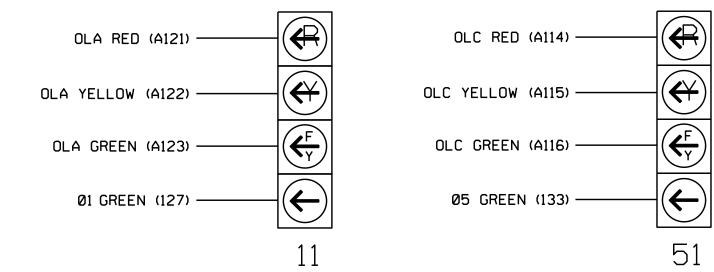
#### SIGNAL HEAD HOOK-UP CHART S2 S3 S4 S5 S6 S7 S8 S9 S10 S11 S12 AUX AUX AUX AUX S5 S6 S6 LOAD SWITCH NO. CMU CHANNEL 8 | 16 | 9 | 10 | 17 8 8 OLA OLB SPARE OLC OLD SPARE PHASE 21,22 NU NU 41,42 P41, P42 51 61,62 NU NU 81,82 NU 11 NU NU 51 NU SIGNAL HEAD NO. RED 102 **\*** 135 108 YELLOW 130 103 136 109 GREEN RED ARROW YELLOW A122 ARROW FLASHING YELLOW ARROW GREEN 133 ARROW 104

NU = Not Used

- \* Denotes install load resistor. See load resistor installation detail this sheet.
- ★ See pictorial of head wiring in detail this sheet.

# FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE

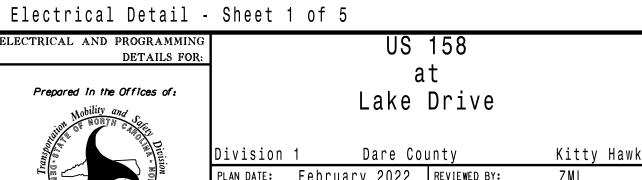
750 N.Greenfield Pkwy, Garner, NC 27529

The sequence display for signal heads 11 and 51 requires special logic programming. See sheet 2 for programming instructions.

#### COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 01-0762 DESIGNED: February 2022 SEALED: 02/24/2022 REVISED:



PLAN DATE: February 2022 REVIEWED BY: ZML

PREPARED BY: JPG REVIEWED BY:

REVISIONS INIT. DATE

DATE

SIG. INVENTORY NO. 01-0762

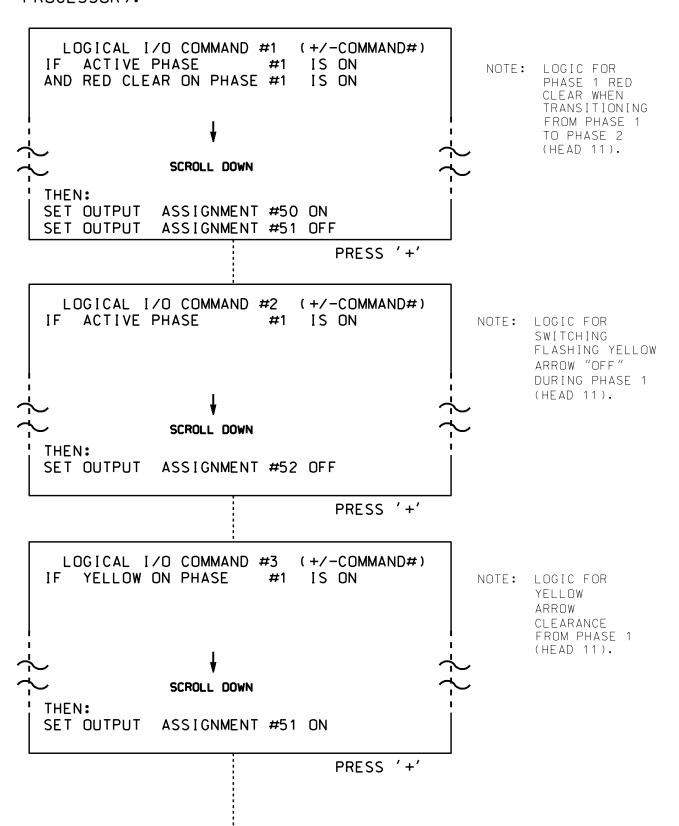
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL

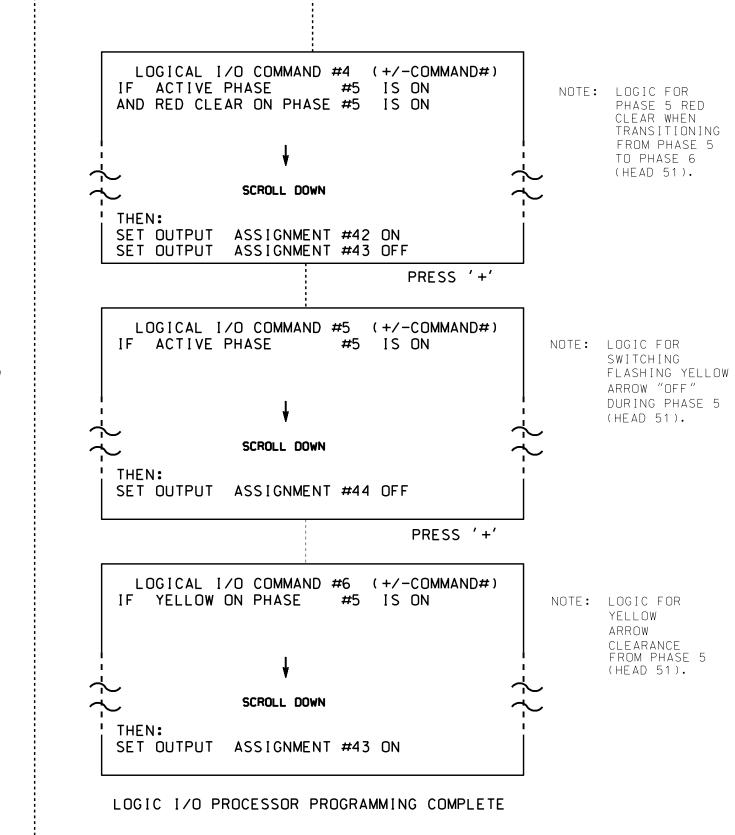
**SIGNATURES COMPLETED** 

# LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5 AND 6.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).





# **OUTPUT REFERENCE SCHEDULE**

OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

### PEDESTRIAN DETECTOR ASSIGNMENT PROGRAMMING DETAIL

(program controller as shown below)

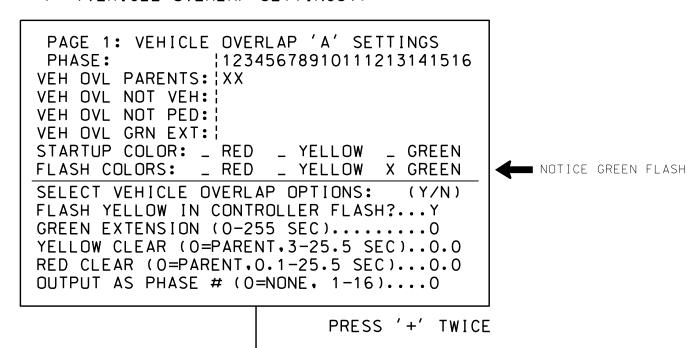
FROM MAIN MENU PRESS '7' (DETECTORS), THEN '2' (PEDESTRIAN DETECTOR ASSIGNMENTS). PRESS '+' UNTIL PED DETECTOR #4 IS REACHED.

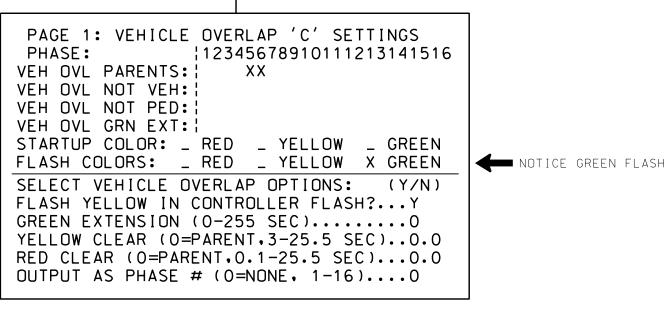
> PED DETECTOR #4 SETTINGS (+/- DET) ¦12345678910111213141516 PHASE# PHASES ASSIGNED | X X SETTING: (Y/N)ENABLE DETECTOR.....Y ENABLE LOGGING.....N ENABLE DIAGNOSTICS.....N RECALL IF FAILED.....N MAX CALLS/MINUTE (0-255)......255 MAX CALLS/DIAG PERIOD (0-255)..... MAX OCCUPANCY % (0-100%).....100

# OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).





OVERLAP PROGRAMMING COMPLETE

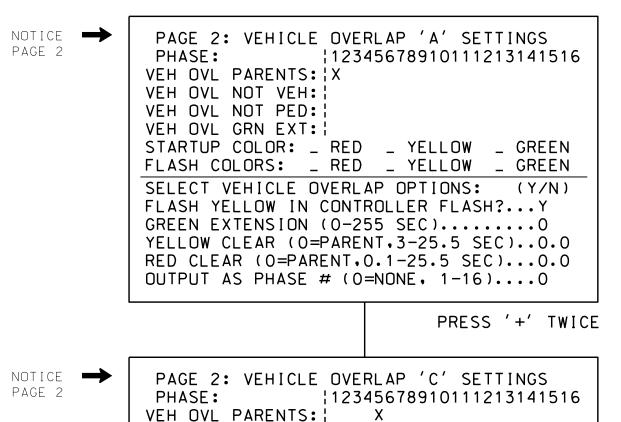
## OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

PROJECT REFERENCE NO.

Sig.1.2

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.



VEH OVL NOT VEH:

VEH OVL NOT PED: :

VEH OVL GRN EXT:

OVERLAP PROGRAMMING COMPLETE

STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN

FLASH COLORS: \_ RED \_ YELLOW \_ GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N)

FLASH YELLOW IN CONTROLLER FLASH?...Y

GREEN EXTENSION (0-255 SEC).....

OUTPUT AS PHASE # (O=NONE, 1-16)....0

YELLOW CLEAR (O=PARENT,3-25.5 SEC)..0.0

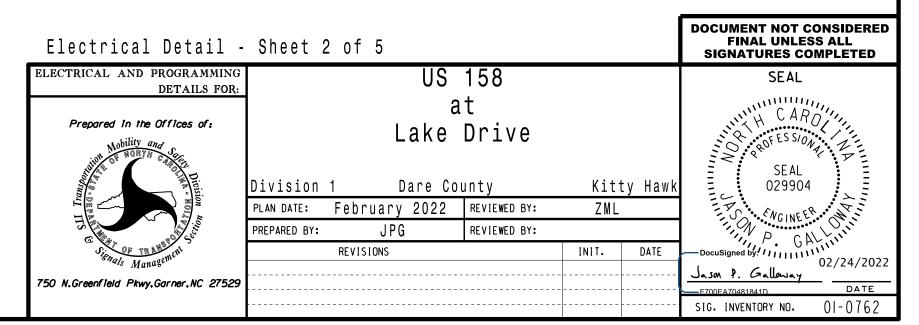
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0

## ADVANCE WALK NOTE

(program controller as shown below)

From Main Menu press '2' (Phase Control), then '1' (Phase Control Functions). Program phases 4 and 8 for 'Advanced Walk'. Make sure the Walk Advance Time shown on the Signal Design plans are programmed in the 'Phase Timing' menu.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 01-0762 DESIGNED: February 2022 SEALED: 02/24/2022 REVISED:



## INPUT PAGE 2 ASSIGNMENT PROGRAMMING DETAIL FOR ALTERNATE PHASING - LOOP 1A

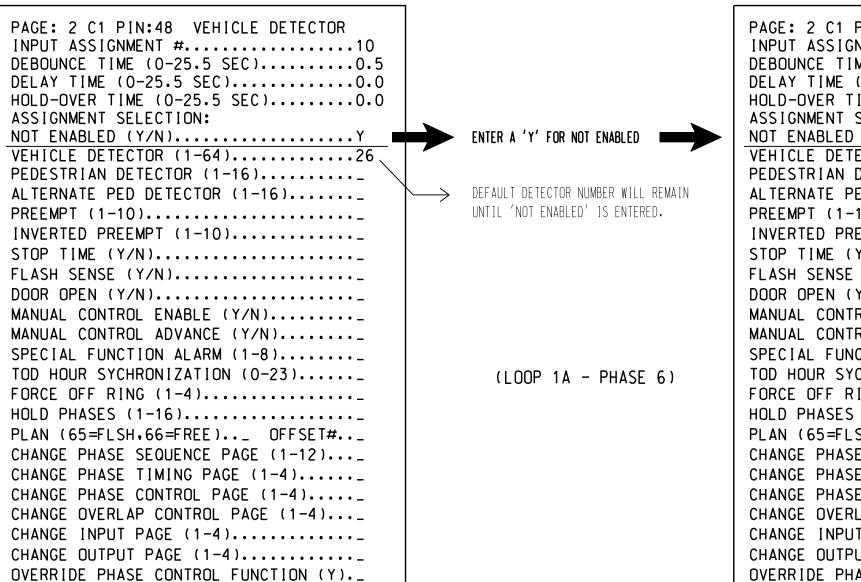
(program controller as shown below)

NOTES: 1. THIS PROGRAMMING APPLIES FOR INPUT PAGE 2 ONLY. INPUT PAGE 1 WILL USE STANDARD DEFAULT SETTINGS. THIS PROGRAMMING IS NECESSARY FOR PROPER DETECTOR OPERATION DURING ALTERNATE PHASING OPERATION.

2. THE FIRST TASK THIS PROGRAMMING ACCOMPLISHES IS THE DISABLING OF INPUT #10 (DETECTOR 26) SO THAT A VEHICLE CALL WILL NOT BE PLACED TO PHASE 6 DURING ALTERNATE PHASING OPERATION. THE SECOND TASK THIS PROGRAMMING ACCOMPLISHES IS THAT IT REASSIGNS DETECTOR 51 TO INPUT #18 SO THAT THE DELAY ON LOOP 1A CAN BE REDUCED FROM 15 SECONDS TO 3 SECONDS.

PRESS '+' TO ADVANCE TO INPUT 18

FROM MAIN MENU PRESS '5' (INPUTS), THEN PRESS 'NEXT' TO GET TO INPUT PAGE '2'. PRESS THE '+' KEY UNTIL INPUT 10 IS REACHED.



PAGE: 2 C1 PIN:48 NOT ENABLED INPUT ASSIGNMENT #.....10 DEBOUNCE TIME (0-25.5 SEC).....0.5 DELAY TIME (0-25.5 SEC)...........0.0 HOLD-OVER TIME (0-25.5 SEC).........0.0 ASSIGNMENT SELECTION: NOT ENABLED (Y/N).....Y VEHICLE DETECTOR (1-64)..... PEDESTRIAN DETECTOR (1-16)..... ALTERNATE PED DETECTOR (1-16)..... PREEMPT (1-10)..... INVERTED PREEMPT (1-10)..... STOP TIME (Y/N)..... FLASH SENSE (Y/N)..... DOOR OPEN (Y/N)..... MANUAL CONTROL ENABLE (Y/N)..... MANUAL CONTROL ADVANCE (Y/N)..... SPECIAL FUNCTION ALARM (1-8)..... TOD HOUR SYCHRONIZATION (0-23)..... FORCE OFF RING (1-4)..... HOLD PHASES (1-16)..... PLAN (65=FLSH,66=FREE)... OFFSET#... CHANGE PHASE SEQUENCE PAGE (1-12)...\_ CHANGE PHASE TIMING PAGE (1-4)..... CHANGE PHASE CONTROL PAGE (1-4).... CHANGE OVERLAP CONTROL PAGE (1-4)...\_ CHANGE INPUT PAGE (1-4)..... CHANGE OUTPUT PAGE (1-4)..... OVERRIDE PHASE CONTROL FUNCTION (Y).\_

PAGE: 2 C1 PIN:56 VEHICLE DETECTOR DEBOUNCE TIME (0-25.5 SEC)...........0.5 DELAY TIME (0-25.5 SEC)............. HOLD-OVER TIME (0-25.5 SEC).........0.0 ASSIGNMENT SELECTION: NOT ENABLED (Y/N).... VEHICLE DETECTOR (1-64).....1 PEDESTRIAN DETECTOR (1-16)..... ALTERNATE PED DETECTOR (1-16)..... PREEMPT (1-10)..... INVERTED PREEMPT (1-10)..... STOP TIME (Y/N)..... FLASH SENSE (Y/N)..... DOOR OPEN (Y/N)..... MANUAL CONTROL ENABLE (Y/N)..... MANUAL CONTROL ADVANCE (Y/N)..... SPECIAL FUNCTION ALARM (1-8)..... TOD HOUR SYCHRONIZATION (0-23)..... FORCE OFF RING (1-4)..... HOLD PHASES (1-16)..... PLAN (65=FLSH.66=FREE)... OFFSET#... CHANGE PHASE SEQUENCE PAGE (1-12)...\_ CHANGE PHASE TIMING PAGE (1-4)..... CHANGE PHASE CONTROL PAGE (1-4).... CHANGE OVERLAP CONTROL PAGE (1-4)...\_ CHANGE INPUT PAGE (1-4)..... CHANGE OUTPUT PAGE (1-4)..... OVERRIDE PHASE CONTROL FUNCTION (Y).\_

PAGE: 2 C1 PIN:56 VEHICLE DETECTOR INPUT ASSIGNMENT #.....18 DEBOUNCE TIME (0-25.5 SEC)..........0.5 DELAY TIME (0-25.5 SEC)...........0.0 HOLD-OVER TIME (0-25.5 SEC)......0.0 ASSIGNMENT SELECTION: NOT ENABLED (Y/N).... VEHICLE DETECTOR (1-64).....51 PEDESTRIAN DETECTOR (1-16)..... ALTERNATE PED DETECTOR (1-16)..... PREEMPT (1-10)..... INVERTED PREEMPT (1-10)..... STOP TIME (Y/N)..... FLASH SENSE (Y/N)..... DOOR OPEN (Y/N)..... MANUAL CONTROL ENABLE (Y/N)..... MANUAL CONTROL ADVANCE (Y/N)..... SPECIAL FUNCTION ALARM (1-8)..... TOD HOUR SYCHRONIZATION (0-23)..... (LOOP 1A - PHASE 1) FORCE OFF RING (1-4)..... HOLD PHASES (1-16)..... PLAN (65=FLSH,66=FREE)... OFFSET#... CHANGE PHASE SEQUENCE PAGE (1-12)...\_ CHANGE PHASE TIMING PAGE (1-4).... CHANGE PHASE CONTROL PAGE (1-4).... CHANGE OVERLAP CONTROL PAGE (1-4)...\_ CHANGE INPUT PAGE (1-4)..... CHANGE OUTPUT PAGE (1-4)..... OVERRIDE PHASE CONTROL FUNCTION (Y).\_

ENTER '51' TO REASSIGN

THE VEHICLE DETECTOR

FOR THIS INPUT

PROJECT REFERENCE NO.

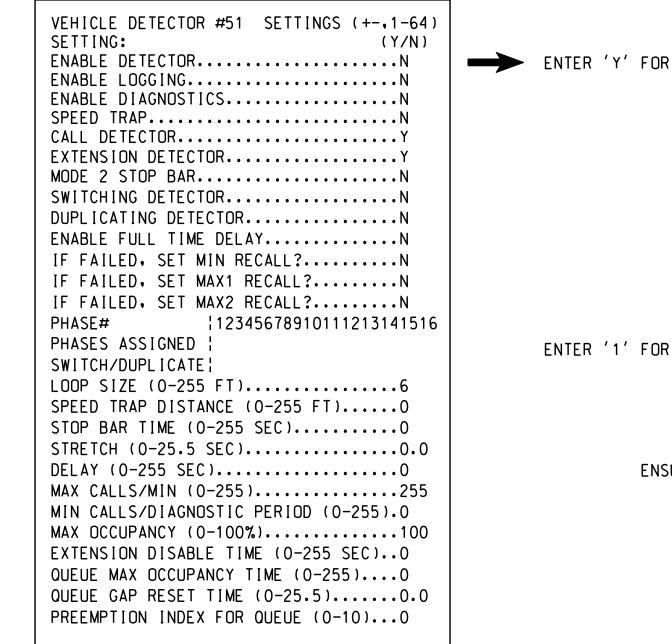
Sig 1.3

PROGRAMMING COMPLETE

#### SPECIAL DETECTOR PROGRAMMING DETAIL - LOOP 1A (ALT.)

#### (program controller as shown below)

FROM MAIN MENU PRESS '7' (DETECTORS), THEN PRESS '1' FOR VEHICLE DETECTORS. PRESS THE '-' KEY TO GET TO VEHICLE DETECTOR #51.

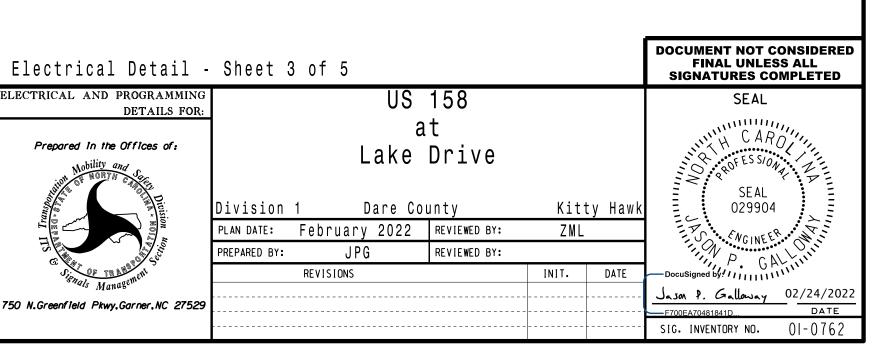


VEHICLE DETECTOR #51 SETTINGS (+-,1-64) SETTING: ENABLE DETECTOR.....Y ENTER 'Y' FOR ENABLE DETECTOR ENABLE LOGGING.....N ENABLE DIAGNOSTICS.....N SPEED TRAP.....N CALL DETECTOR.....Y EXTENSION DETECTOR.....Y MODE 2 STOP BAR.....N SWITCHING DETECTOR.....N DUPLICATING DETECTOR.....N ENABLE FULL TIME DELAY.....N IF FAILED, SET MIN RECALL?..... IF FAILED, SET MAX1 RECALL?.....N IF FAILED, SET MAX2 RECALL?.....N PHASE# 12345678910111213141516 PHASES ASSIGNED |X ENTER '1' FOR PHASES ASSIGNED SWITCH/DUPLICATE; LOOP SIZE (0-255 FT).....6 SPEED TRAP DISTANCE (0-255 FT).....0 STOP BAR TIME (0-255 SEC)...... STRETCH (0-25.5 SEC)................0.0 ENSURE DELAY IS '3' MAX CALLS/MIN (0-255)......255 MIN CALLS/DIAGNOSTIC PERIOD (0-255).0 MAX OCCUPANCY (0-100%).....100 EXTENSION DISABLE TIME (0-255 SEC)..0 QUEUE MAX OCCUPANCY TIME (0-255)....0 QUEUE GAP RESET TIME (0-25.5).....0.0 PREEMPTION INDEX FOR QUEUE (0-10)...0

DETECTOR PROGRAMMING COMPLETE

NOTE: DETECTOR IS PROGRAMMED PER THE INPUT FILE CONNECTION AND PROGRAMMING CHART SHOWN ON SHEET 1.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 01-0762 DESIGNED: February 2022 SEALED: 02/24/2022 REVISED:



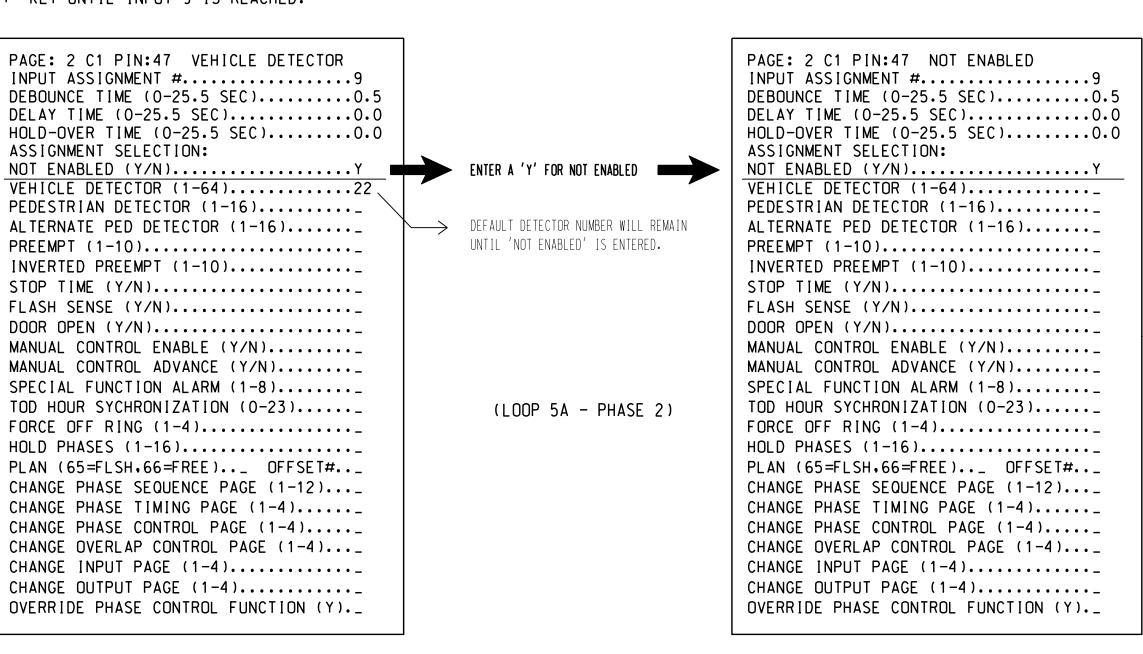
# INPUT PAGE 2 ASSIGNMENT PROGRAMMING DETAIL FOR ALTERNATE PHASING - LOOP 5A

(program controller as shown below)

- NOTES: 1. THIS PROGRAMMING APPLIES FOR INPUT PAGE 2 ONLY. INPUT PAGE 1 WILL USE STANDARD DEFAULT SETTINGS. THIS PROGRAMMING IS NECESSARY FOR PROPER DETECTOR OPERATION DURING ALTERNATE PHASING OPERATION.
  - 2. THE FIRST TASK THIS PROGRAMMING ACCOMPLISHES IS THE DISABLING OF INPUT #9 (DETECTOR 22) SO THAT A VEHICLE CALL WILL NOT BE PLACED TO PHASE 2 DURING ALTERNATE PHASING OPERATION. THE SECOND TASK THIS PROGRAMMING ACCOMPLISHES IS THAT IT REASSIGNS DETECTOR 55 TO INPUT #17 SO THAT THE DELAY ON LOOP 5A CAN BE REDUCED FROM 15 SECONDS TO 3 SECONDS.

PRESS '+' TO ADVANCE TO INPUT 17

FROM MAIN MENU PRESS '5' (INPUTS). THEN PRESS 'NEXT' TO GET TO INPUT PAGE '2'. PRESS THE '+' KEY UNTIL INPUT 9 IS REACHED.



DEBOUNCE TIME (0-25.5 SEC)...........0.5 DELAY TIME (0-25.5 SEC)...........0.0 HOLD-OVER TIME (0-25.5 SEC).........0.0 ASSIGNMENT SELECTION: NOT ENABLED (Y/N)..... VEHICLE DETECTOR (1-64).....5 PEDESTRIAN DETECTOR (1-16)..... ALTERNATE PED DETECTOR (1-16)..... PREEMPT (1-10)..... INVERTED PREEMPT (1-10)..... STOP TIME (Y/N)..... FLASH SENSE (Y/N)..... DOOR OPEN (Y/N)..... MANUAL CONTROL ENABLE (Y/N)..... MANUAL CONTROL ADVANCE (Y/N)..... SPECIAL FUNCTION ALARM (1-8)..... TOD HOUR SYCHRONIZATION (0-23)..... FORCE OFF RING (1-4)..... HOLD PHASES (1-16)..... PLAN (65=FLSH.66=FREE)... OFFSET#... CHANGE PHASE SEQUENCE PAGE (1-12)...\_ CHANGE PHASE TIMING PAGE (1-4)..... CHANGE PHASE CONTROL PAGE (1-4)..... CHANGE OVERLAP CONTROL PAGE (1-4)...\_ CHANGE INPUT PAGE (1-4)..... CHANGE OUTPUT PAGE (1-4)..... OVERRIDE PHASE CONTROL FUNCTION (Y).\_

PAGE: 2 C1 PIN:55 VEHICLE DETECTOR

PAGE: 2 C1 PIN:55 VEHICLE DETECTOR DEBOUNCE TIME (0-25.5 SEC)...........0.5 DELAY TIME (0-25.5 SEC)............0.0 HOLD-OVER TIME (0-25.5 SEC).........0.0 ASSIGNMENT SELECTION: NOT ENABLED (Y/N)..... VEHICLE DETECTOR (1-64)......55 PEDESTRIAN DETECTOR (1-16).... ALTERNATE PED DETECTOR (1-16)..... PREEMPT (1-10)..... INVERTED PREEMPT (1-10)..... STOP TIME (Y/N)..... FLASH SENSE (Y/N)..... DOOR OPEN (Y/N)..... MANUAL CONTROL ENABLE (Y/N)..... MANUAL CONTROL ADVANCE (Y/N)..... SPECIAL FUNCTION ALARM (1-8)..... TOD HOUR SYCHRONIZATION (0-23)..... (LOOP 5A - PHASE 5) FORCE OFF RING (1-4)..... HOLD PHASES (1-16)..... PLAN (65=FLSH,66=FREE)... OFFSET#... CHANGE PHASE SEQUENCE PAGE (1-12)...\_ CHANGE PHASE TIMING PAGE (1-4)..... CHANGE PHASE CONTROL PAGE (1-4).... CHANGE OVERLAP CONTROL PAGE (1-4)... CHANGE INPUT PAGE (1-4)..... CHANGE OUTPUT PAGE (1-4)..... OVERRIDE PHASE CONTROL FUNCTION (Y).\_

ENTER '55' TO REASSIGN

THE VEHICLE DETECTOR

FOR THIS INPUT

PROJECT REFERENCE NO.

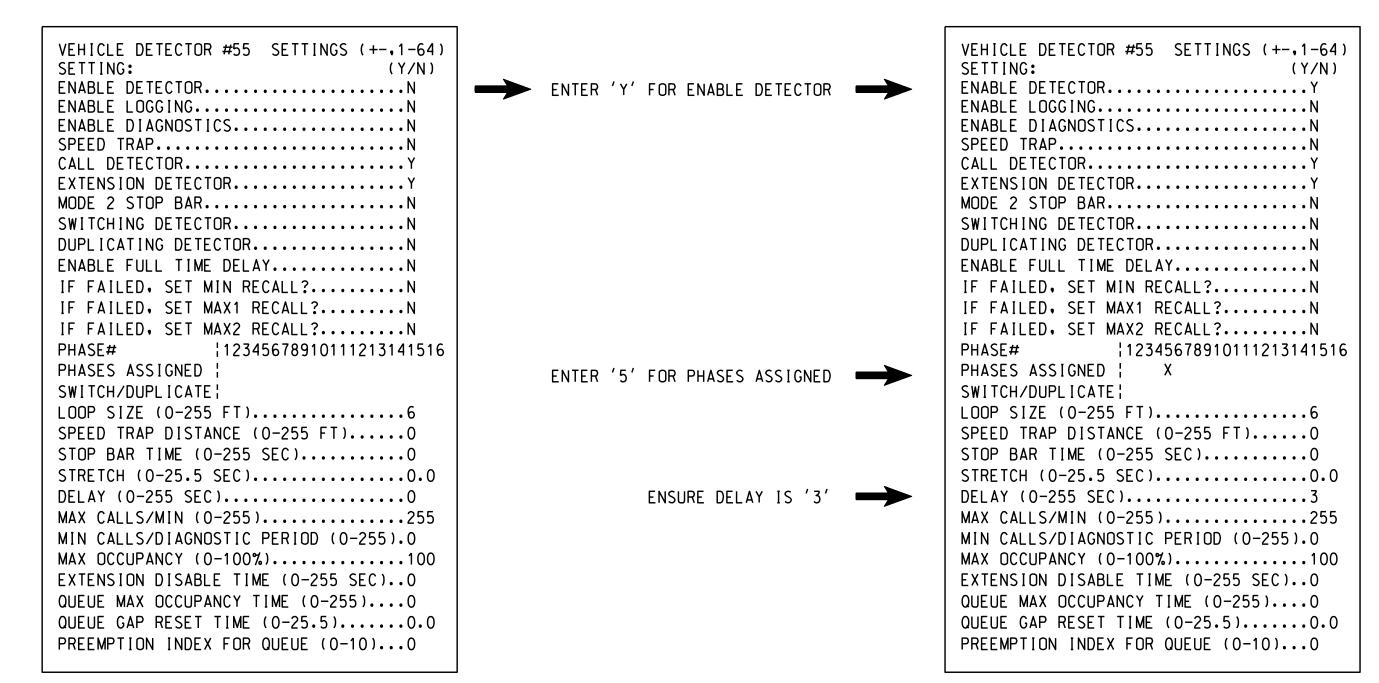
Sig.1.4

PROGRAMMING COMPLETE

#### SPECIAL DETECTOR PROGRAMMING DETAIL - LOOP 5A (ALT.)

#### (program controller as shown below)

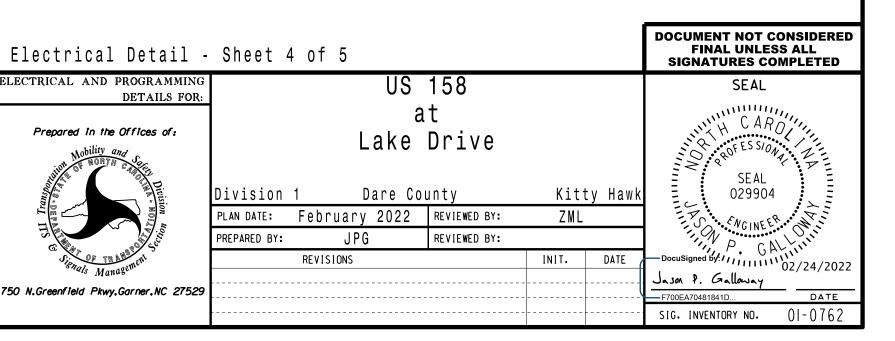
FROM MAIN MENU PRESS '7' (DETECTORS), THEN PRESS '1' FOR VEHICLE DETECTORS. PRESS THE '-' KEY TO GET TO VEHICLE DETECTOR #55.



DETECTOR PROGRAMMING COMPLETE

NOTE: DETECTOR IS PROGRAMMED PER THE INPUT FILE CONNECTION AND PROGRAMMING CHART SHOWN ON SHEET 1.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 01-0762 DESIGNED: February 2022 SEALED: 02/24/2022 REVISED:



PROJECT REFERENCE NO. Sig 1.5

# ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING COORDINATION - SELECT ALL PAGE CHANGES (AS SHOWN BELOW) WITHIN COORDINATION PLAN PROGRAMMING.

TO RUN ALT. PHASING DURING <u>FREE RUN</u> - PROGRAM PAGE CHANGES (SHOWN BELOW) IN SEPARATE TIME OF DAY EVENTS. IF PAGE 1 IS USED, NO EVENT PROGRAMMING IS NECESSARY FOR THAT PARTICULAR PAGE.

ACTIVE PAGES REQUIRED TO RUN DEFAULT PHASING	1	1
ACTIVE PAGES REQUIRED TO RUN <u>ALTERNATE PHASING</u>	2	2

NOTE: PAGES NOT SHOWN (i.e. sequence, phase control, etc.) SHOULD REMAIN AS '1', OR AS DEFINED BY TIMING ENGINEER.

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY PAGE CHANGE EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN PAGE CHANGE EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

## ALTERNATE PHASING PAGE CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN THESE OVERLAP/INPUT PAGE CHANGES ACTIVATE TO CALL THE "ALTERNATE PHASING":

OVERLAPS PAGE 2: Modifies overlap parent phases for heads 11 and 51 to run protected

turns only.

INPUTS PAGE 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1

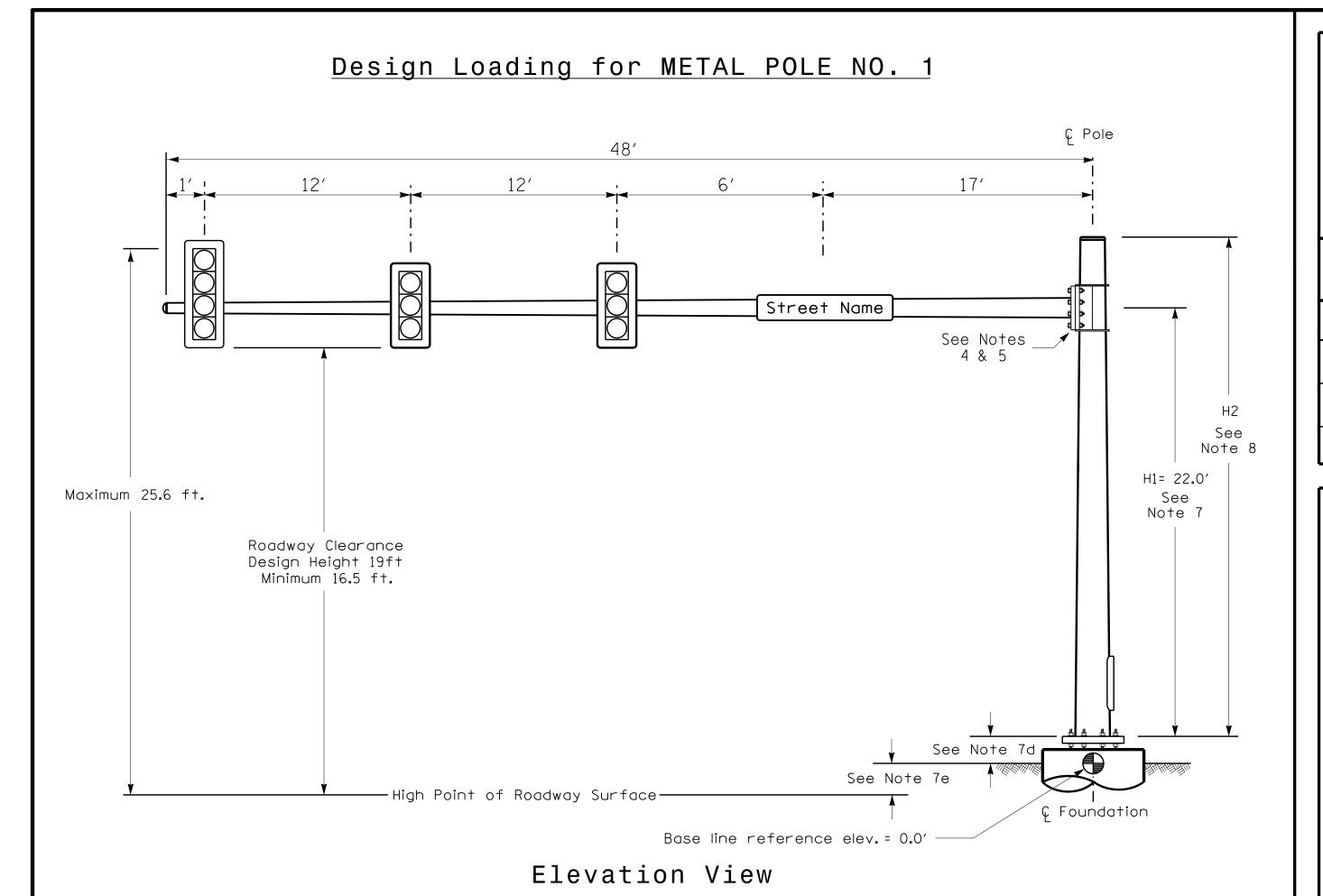
call on loop 1A to 3 seconds.

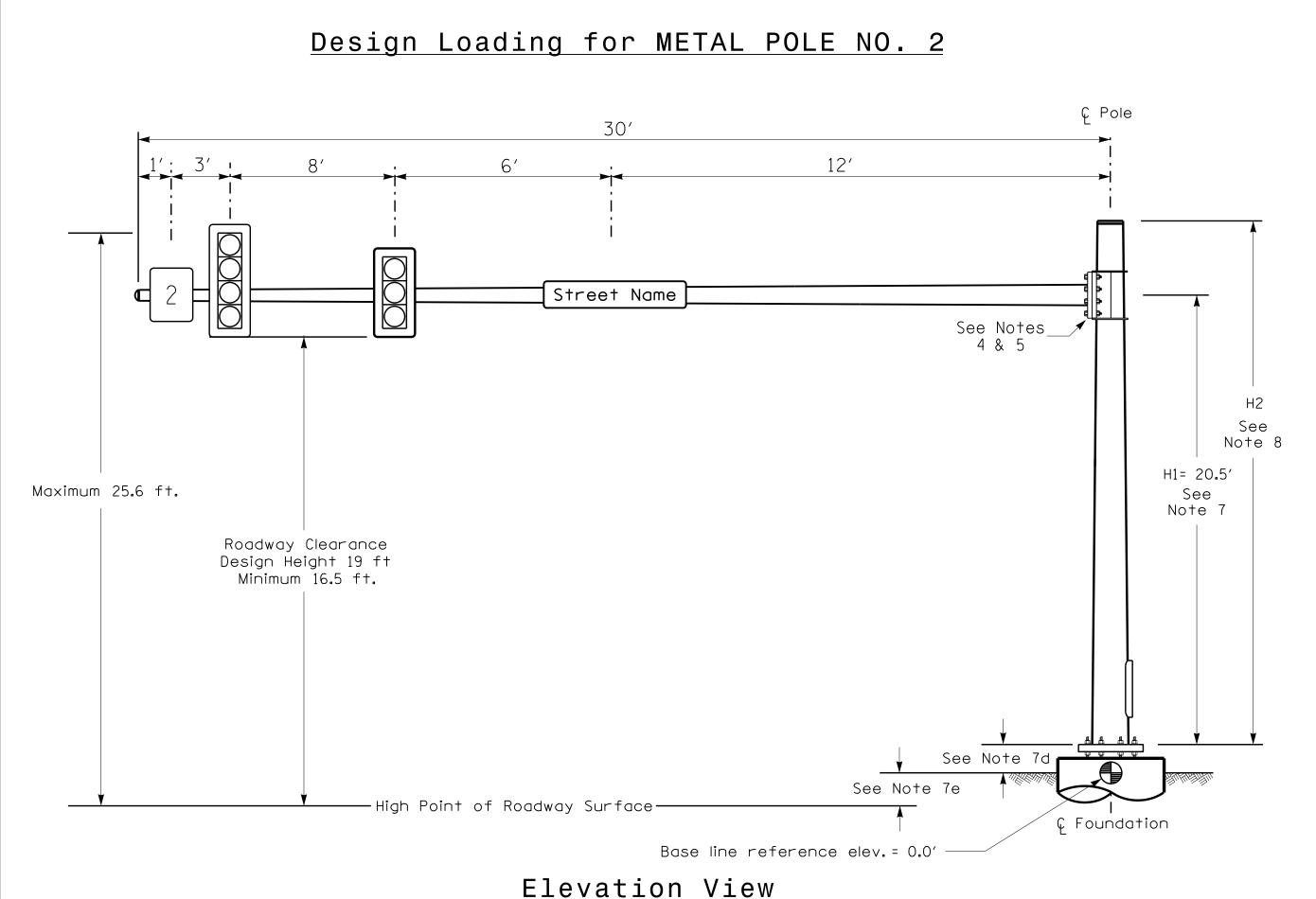
Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 3 seconds.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 01-0762 DESIGNED: February 2022 SEALED: 02/24/2022 REVISED:

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED Electrical Detail - Sheet 5 of 5 ELECTRICAL AND PROGRAMMING US 158 DETAILS FOR: Prepared in the Offices of: Lake Drive Division 1 Dare County Kitty Hawk PLAN DATE: February 2022 REVIEWED BY: ZMLPREPARED BY: JPG REVIEWED BY: REVISIONS INIT. DATE 750 N.Greenfield Pkwy.Garner.NC 27529

SIG. INVENTORY NO. 01-0762



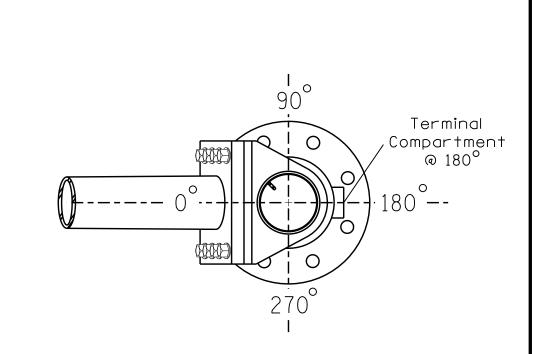


## SPECIAL NOTE

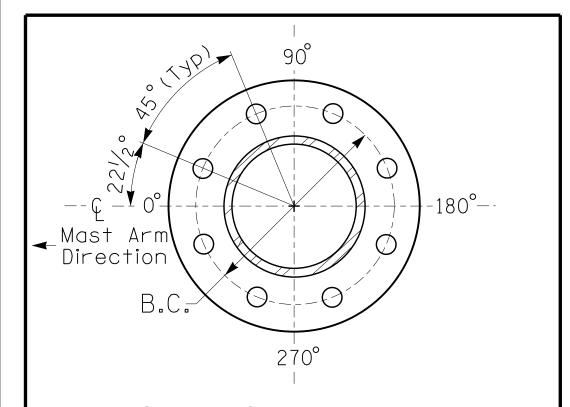
The contractor is responsible for verifying that the mast arm attachment height (H1) will provide the "Design Height" clearance from the roadway before submitting final shop drawings for approval. Verify elevation data below which was obtained by field measurement or from available project survey data.

# Elevation Data for Mast Arm Attachment (H1)

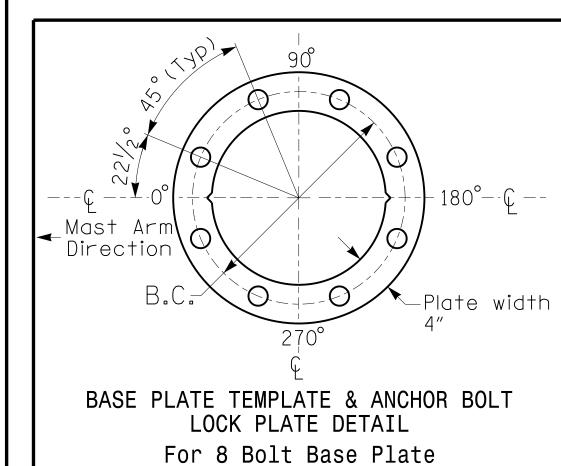
Elevation Differences for:	Pole 1	Pole 2
Baseline reference point at © Foundation @ ground level	0.0 ft.	0.0 ft.
Elevation difference at High point of roadway surface	+0.9 ft.	-0.8 ft.
Elevation difference at Edge of travelway or face of curb	N/A	N/A



POLE RADIAL ORIENTATION



8 BOLT BASE PLATE DETAIL
See Note 6



METAL POLE No. 1 and 2

PROJECT	REFERENCE	NO.	SHEET NO.
			Sig 1.6

	MAST ARM LOADING SC	HEDU	LE	
LOADING SYMBOL	DESCRIPTION	AREA	SIZE	WEIGHT
	RIGID MOUNTED SIGNAL HEAD 12"-4 SECTION-WITH BACKPLATE	11.5 S.F.	25.5″W X 66.0″L	74 LBS
	RIGID MOUNTED SIGNAL HEAD 12"-3 SECTION-WITH BACKPLATE	9.3 S.F.	25.5″W X 52.5″L	60 LBS
2	SIGN RIGID MOUNTED	7.5 S.F.	30.0" W X 36.0"L	14 LBS
Street Name	STREET NAME SIGN RIGID MOUNTED	16.0 S.F.	24.0" W X 96.0"L	36 LBS

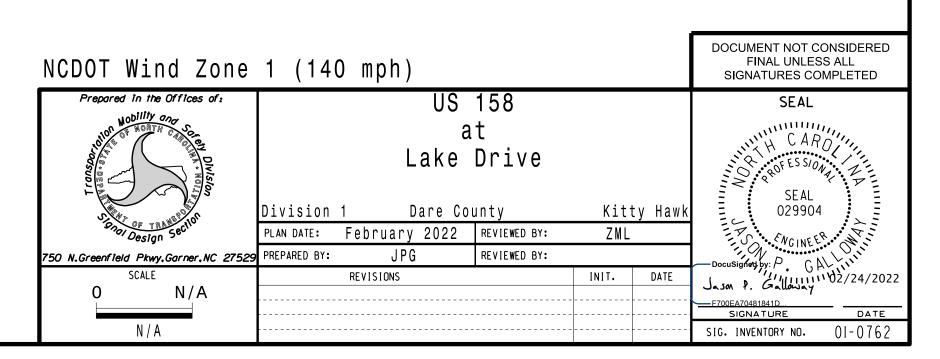
#### <u>NOTES</u>

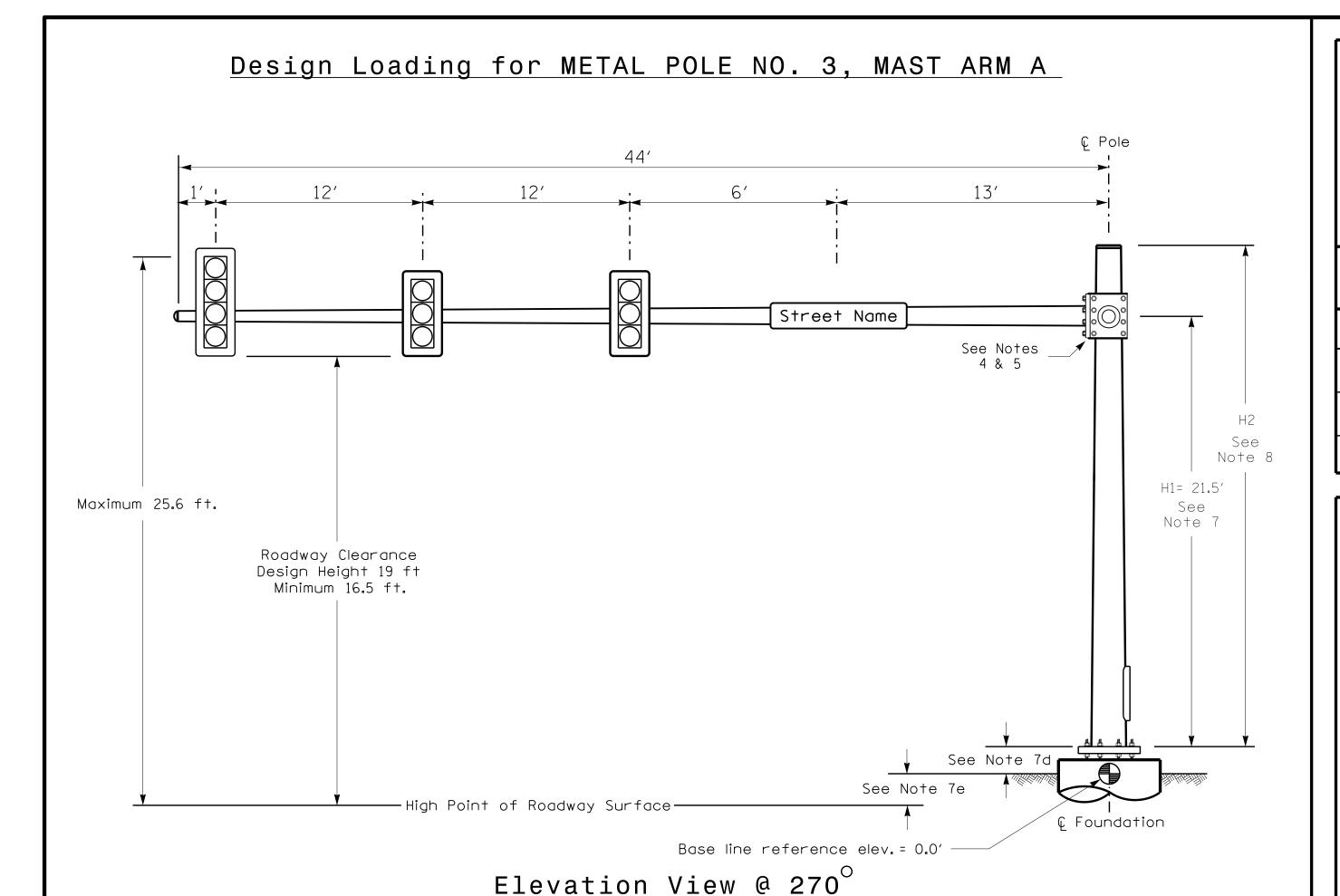
#### DESIGN REFERENCE MATERIAL

- 1. Design the traffic signalstructure and foundation in accordance with:
- The 6th Edition 2013 AASHTO "Standard Specifications for Structural Supports for Highway Signs, Luminaires, and Traffic Signals, including all of the latest interim revisions.
- The 2018 NCDOT "Standard Specifications for Roads and Structures." The latest addenda to the specifications can be found in the traffic signal project special provisions.
- The 2018 NCDOT Roadway Standard Drawings.
- The traffic signal project plans and special provisions.
- The NCDOT "MetalPole Standards" located at the following NCDOT website: https://connect.ncdot.gov/resources/safety/Pages/ITS-Design-Resources.aspx

#### DESIGN REQUIREMENTS

- Design the traffic signal structure using the loading conditions shown in the elevation views. These are anticipated worst case "design loads" and may not represent the actual loads that will be applied at the time of the installation. The contractor should refer to the traffic signal plans for the actual loads that will be applied at the time of the installation.
   Design all signal supports using stress ratios that do not exceed 0.9.
- 4. The camber design for the mast arm deflection should provide an appearance of a low pitched arch where the tip or the free end of the mast arm does not deflect below horizontal when fully loaded.
- 5. A clamp-type bolted mast arm-to-pole connection may be used instead of the welded ring stiffened box connection shown as long as the connection meets all of the design requirements.
- 6. Design base plate with 8 anchor bolt holes. Provide 2 inch  $\times$  60 inch anchor bolts.
- 7. The mast arm attachment height (H1) shown is based on the following design assumptions: a. Mast arm slope and deflection are not considered in determining the arm attachment height as they are assumed to offset each other.
- b. Signalheads are rigidly mounted and vertically centered on the mast arm.
- c. The roadway clearance height for design is as shown in the elevation views.
- d. The top of the pole base plate is 0.75 feet above the ground elevation.
- e. Refer to the Elevation Data Chart for the elevation differences between the proposed foundation ground leveland the high point of the roadway.
- 8. The pole manufacturer will determine the total height (H2) of each pole using the greater of the following:
- Mast arm attachment height (H1) plus 2 feet, or
- H1 plus 1/2 of the totalheight of the mast arm attachment assembly plus 1 foot.
- 9. If pole location adjustments are required, the contractor must gain approval from the Engineer as this may affect the mast arm lengths and arm attachment heights. The contractor may contact the Signal Design Section Senior Structural Engineer for assistance at (919) 814-5000.
- 10.The contractor is responsible for verifying that the mast arm length shown will allow proper positioning of the signal heads over the roadway.
- 11. The contractor is responsible for providing soilpenetration testing data (SPT) to the pole manufacturer so site specific foundations can be designed.





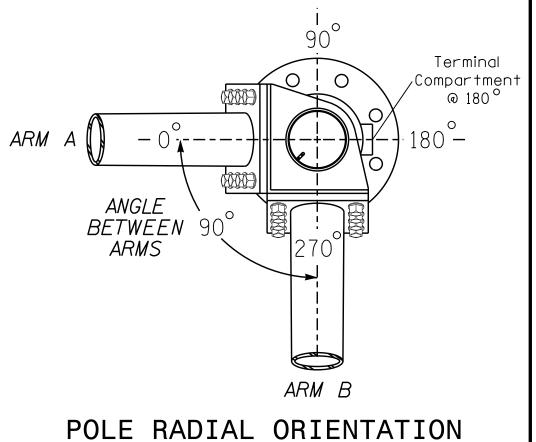
# Design Loading for METAL POLE NO. 3, MAST ARM B Ç Pole Street Name See Notes 4 & 5 Н2 See Note 8 H1= 21.5' Maximum 25.6 ft. See Note Roadway Clearance Design Height 19 ft Minimum 16.5 ft. See Note 7d See Note 7e -High Point of Roadway Surface-G Foundation Base line reference elev. = 0.0' Elevation View @ 0

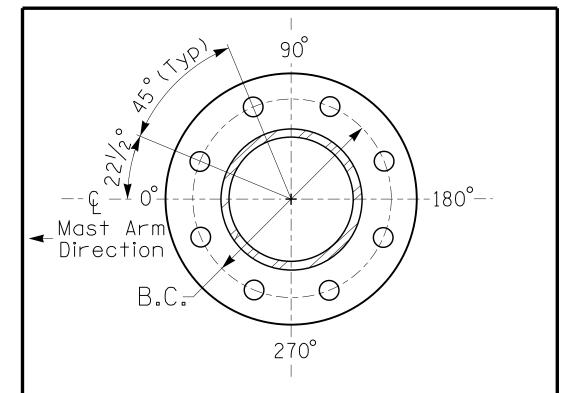
## SPECIAL NOTE

The contractor is responsible for verifying that the mast arm attachment height (H1) will provide the "Design Height" clearance from the roadway before submitting final shop drawings for approval. Verify elevation data below which was obtained by field measurement or from available project survey data.

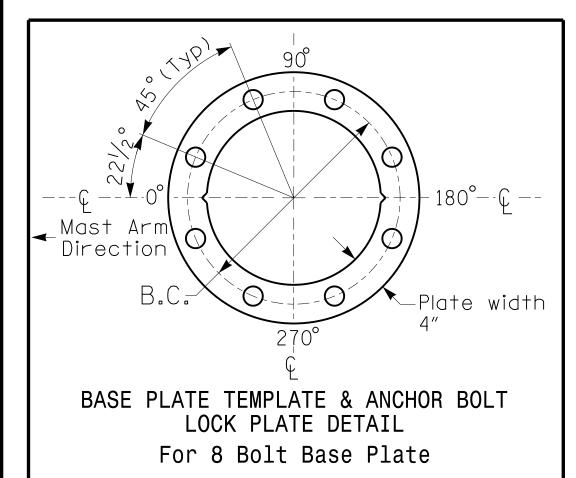
# Elevation Data for Mast Arm Attachment (H1)

Elevation Differences for:	Arm "A"	Arm "B"
Baseline reference point at © Foundation @ ground level	0.0 ft.	0.0 ft.
Elevation difference at High point of roadway surface	+0.5 ft.	-0.7 ft.
Elevation difference at Edge of travelway or face of curb	N/A	N/A





# 8 BOLT BASE PLATE DETAIL See Note 6



METAL POLE No. 3

PROJECT REFERENCE NO. SHEET NO. Sig.1.7

	MAST ARM LOADING SC	HEDU	LE	
loading Symbol	DESCRIPTION	AREA	SIZE	WEIGHT
	RIGID MOUNTED SIGNAL HEAD 12"-4 SECTION-WITH BACKPLATE	11.5 S.F.	25.5″W X 66.0″L	74 LBS
	RIGID MOUNTED SIGNAL HEAD 12"-3 SECTION-WITH BACKPLATE	9.3 S.F.	25.5″W X 52.5″L	60 LBS
2	SIGN RIGID MOUNTED	7.5 S.F.	30.0"W X 36.0"L	14 LBS
Street Name	STREET NAME SIGN RIGID MOUNTED	16.0 S.F.	24.0" W X 96.0"L	36 LBS

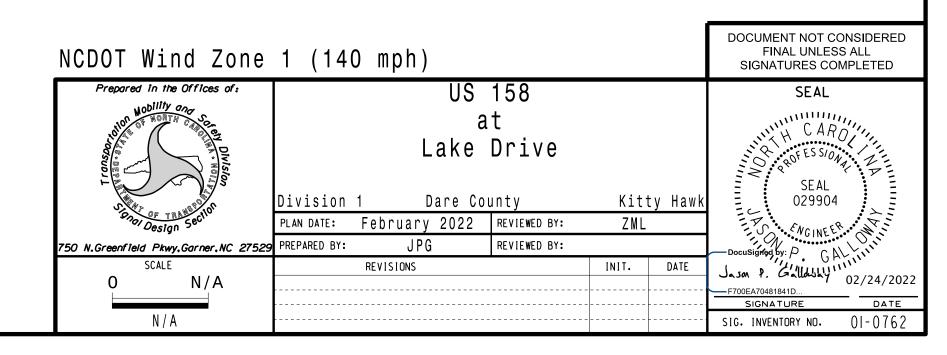
# <u>NOTES</u>

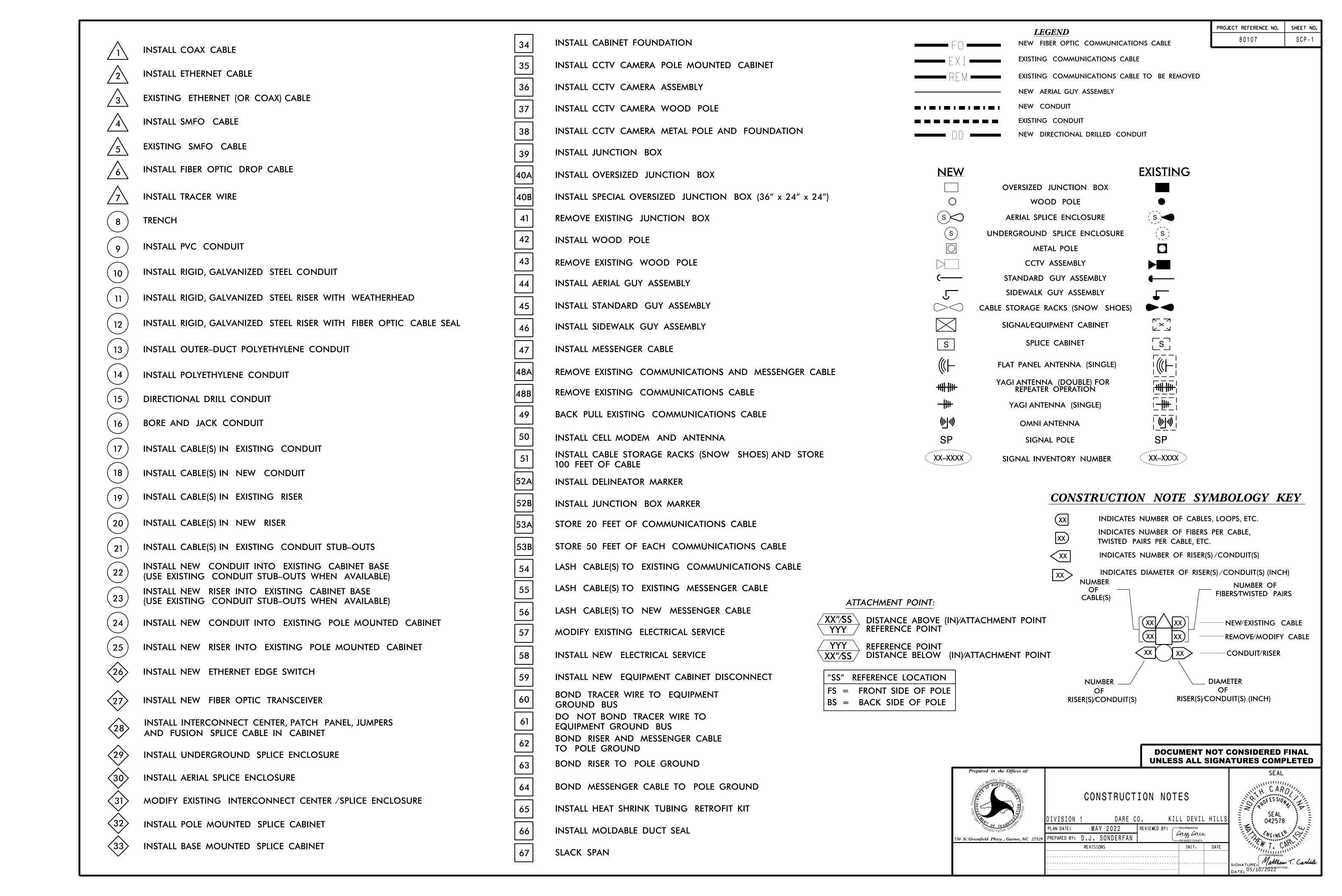
#### DESIGN REFERENCE MATERIAL

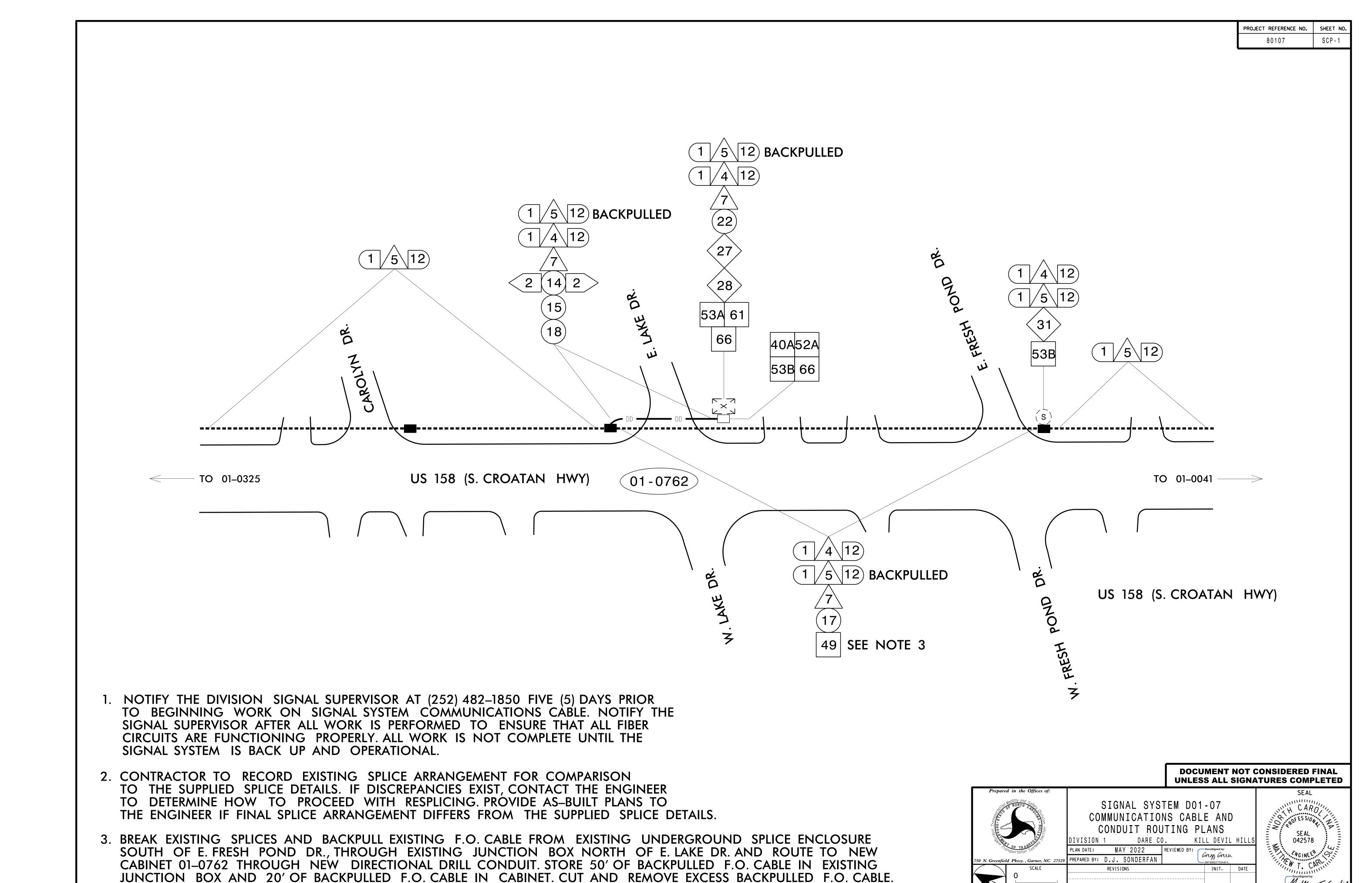
- 1. Design the traffic signal structure and foundation in accordance with:
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#### DESIGN REQUIREMENTS

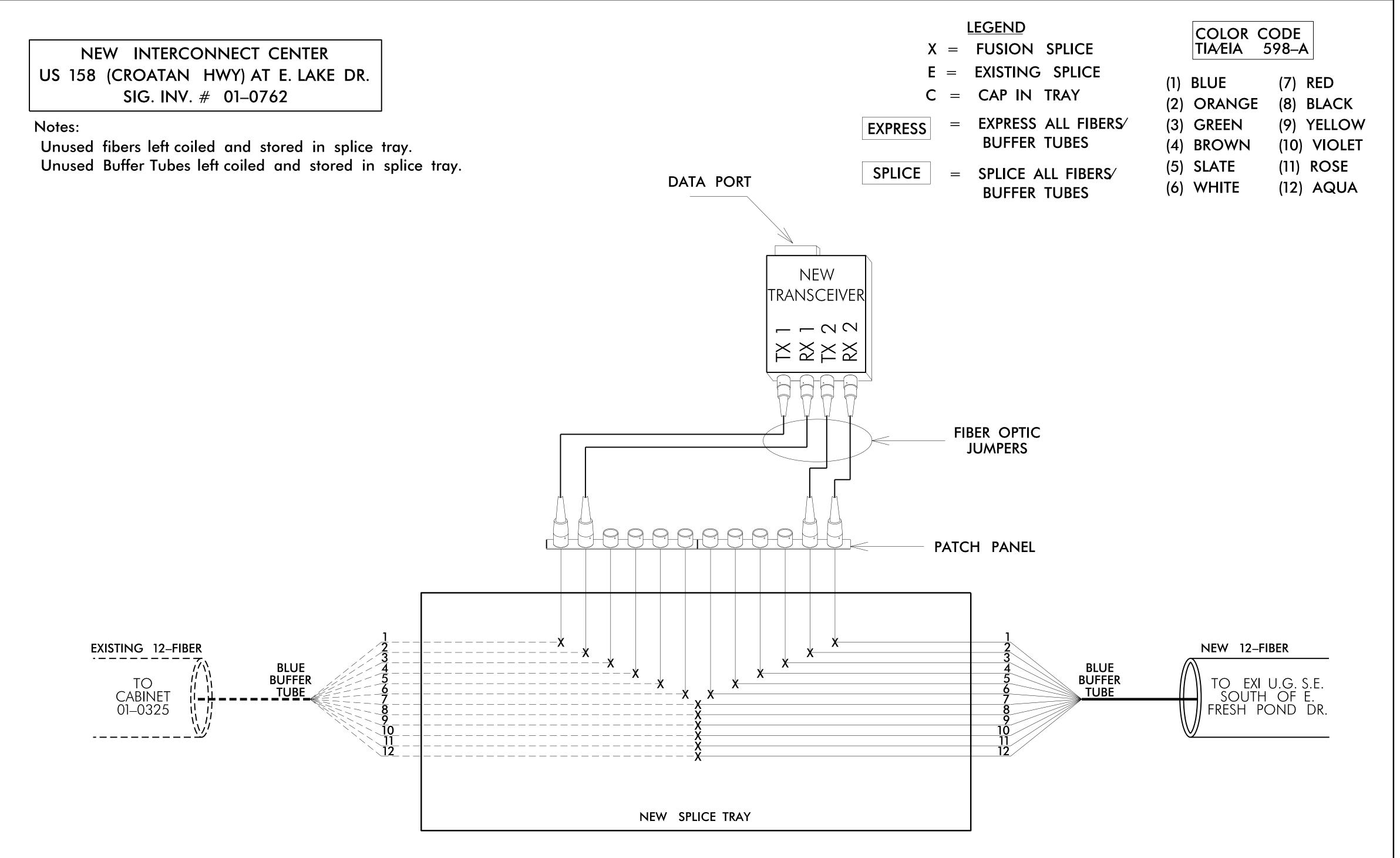
- 2. Design the traffic signal structure using the loading conditions shown in the elevation views. These are anticipated worst case "design loads" and may not represent the actual loads that will be applied at the time of the installation. The contractor should refer to the traffic signal plans for the actual loads that will be applied at the time of the installation.
- 3. Design all signal supports using stress ratios that do not exceed 0.9.
- 4. The camber design for the mast arm deflection should provide an appearance of a low pitched arch where the tip or the free end of the mast arm does not deflect below horizontal when fully loaded.
- 5. A clamp-type bolted mast arm-to-pole connection may be used instead of the welded ring stiffened box connection shown as long as the connection meets all of the design requirements. This requires staggering the connections. Use elevation data for each arm to determine appropriate arm connection points.
- 6. Design base plate with 8 anchor bolt holes. Provide 2 inch x 60 inch anchor bolts.
- 7. The mast arm attachment height (H1) shown is based on the following design assumptions:
  a. Mast arm slope and deflection are not considered in determining the arm attachment
  height as they are assumed to offset each other.
  - b. Signal heads are rigidly mounted and vertically centered on the mast arm.
- c. The roadway clearance height for design is as shown in the elevation views.
- d. The top of the pole base plate is 0.75 feet above the ground elevation.
- e. Refer to the Elevation Data Chart for the elevation differences between the proposed foundation ground level and the high point of the roadway.
- 8. The pole manufacturer will determine the total height (H2) of each pole using the greater of the following:
- Mast arm attachment height (H1) plus 2 feet, or
- H1 plus 1/2 of the total height of the mast arm attachment assembly plus 1 foot.
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- 10. The contractor is responsible for verifying that the mast arm length shown will allow proper positioning of the signal heads over the roadway.
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PROJECT REFERENCE NO. SHEET NO. 80107 SCP-3



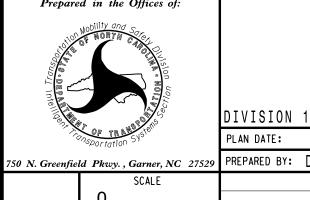
- 1. NOTIFY THE DIVISION SIGNAL SUPERVISOR AT (252) 482–1850 FIVE (5) DAYS PRIOR TO BEGINNING WORK ON SIGNAL SYSTEM COMMUNICATIONS CABLE. NOTIFY THE SIGNAL SUPERVISOR AFTER ALL WORK IS PERFORMED TO ENSURE THAT ALL FIBER CIRCUITS ARE FUNCTIONING PROPERLY. ALL WORK IS NOT COMPLETE UNTIL THE SIGNAL SYSTEM IS BACK UP AND OPERATIONAL.
- 2. CONTRACTOR TO RECORD EXISTING SPLICE ARRANGEMENT FOR COMPARISON TO THE SUPPLIED SPLICE DETAILS. IF DISCREPANCIES EXIST, CONTACT THE ENGINEER TO DETERMINE HOW TO PROCEED WITH RESPLICING. PROVIDE AS-BUILT PLANS TO THE ENGINEER IF FINAL SPLICE ARRANGEMENT DIFFERS FROM THE SUPPLIED SPLICE DETAILS.
- 3. TRANSCEIVER TERMINATION CONFIGURATIONS ARE GENERIC. CONTRACTOR IS RESPONSIBLE FOR DETERMINING \ ENSURING PROPER TERMINATIONS.
- 4. INCLUDE ON THE COVER OF EACH SPLICE TRAY THE FOLLOWING: REFERENCE SECTION 1731 "FIBER OPTIC SPLICE ENCLOSURE"
  - 1) SPLICE LOCATION
  - 2) DATE
  - 3) COMPANY NAME
    4) NAME OF INDIVIDUAL PERFORMING THE SPLICING

PRIOR TO INSTALLING THE COVER ON THE SPLICE TRAY TAKE A DIGITAL PHOTOGRAPH SHOWING THE SPLICE TRAY AND INFORMATION SHOWN ABOVE (1–4) AND SUBMIT PHOTOGRAPH ALONG WITH OTDR TEST RESULTS.

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared in the Offices of:

SEAL



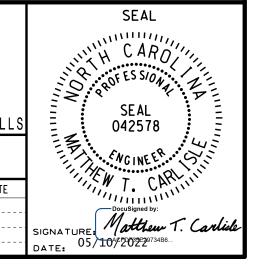
SPLICE DETAIL

DIVISION 1 DARE CO. KILL DEVIL HILLS

PLAN DATE: MAY 2022 REVIEWED BY: Gray Gray

PREPARED BY: D.J. SONDERFAN

REVISIONS INIT. DATE



PROJECT REFERENCE NO.

**LEGEND** 

X = FUSION SPLICE

COLOR CODE TIA/EIA 598-A

E = EXISTING SPLICE

(1) BLUE (7) RED

C = CAP IN TRAY

(2) ORANGE (8) BLACK (3) GREEN

(9) YELLOW

EXPRESS ALL FIBERS/ **EXPRESS BUFFER TUBES** 

(4) BROWN

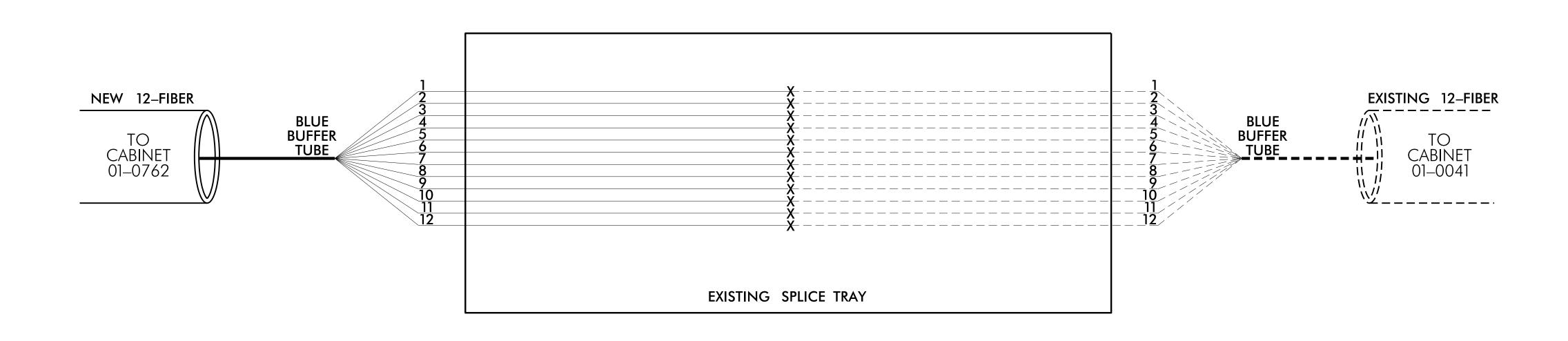
(10) VIOLET (11) ROSE

**SPLICE** 

= SPLICE ALL FIBERS/ **BUFFER TUBES** 

(5) SLATE (6) WHITE

(12) AQUA



- 1. NOTIFY THE DIVISION SIGNAL SUPERVISOR AT (252) 482–1850 FIVE (5) DAYS PRIOR TO BEGINNING WORK ON SIGNAL SYSTEM COMMUNICATIONS CABLE. NOTIFY THE SIGNAL SUPERVISOR AFTER ALL WORK IS PERFORMED TO ENSURE THAT ALL FIBER CIRCUITS ARE FUNCTIONING PROPERLY. ALL WORK IS NOT COMPLETE UNTIL THE SIGNAL SYSTEM IS BACK UP AND OPERATIONAL.
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EXISTING UNDERGROUND

SPLICE ENCLOSURE SOUTH OF

E. FRESH POND DR.

Unused fibers left coiled and stored in splice tray.

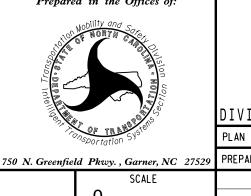
Unused Buffer Tubes left coiled and stored in splice tray.

Notes:

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- 2) DATE
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DIVISION 1 KILL DEVIL HILLS DARE CO. PLAN DATE: MAY 2022 Gregg Green PREPARED BY: D.J. SONDERFAN REVISIONS

SPLICE DETAIL

